



Why choose a VSIL from Triakis instead of a simulator from another company?

Virtual System Integration Lab (VSIL) Cost-Saving Benefit	<i>Triakis</i>	Others
100% virtual, total environment simulation.	<i>Yes</i>	<i>No</i>
Accepted by the FAA and the EASA as a complete software qualification environment to DO-178B levels A to E. No hardware required!	<i>Yes</i>	<i>No</i>
Proven ability to consistently produce zero-defect avionics software.	<i>Yes</i>	<i>No</i>
Provides three levels of development support: <ul style="list-style-type: none"> • System executable specifications • Pseudo-processor w/RTOS emulation • Instruction set emulation Maintains flow-down verification test compatibility with all three.	<i>Yes</i>	<i>No</i>
System-level, and software-level verification test consistency method validated by NASA software assurance research.	<i>Yes</i>	<i>No</i>
Allows you to run MCDC- tracking instrumented code with no timing penalty (runs as though it were uninstrumented).	<i>Yes</i>	<i>No</i>
Virtual system extends as far as you need to verify target avionics system and software behavior in response to external stimuli and other influences.	<i>Yes</i>	<i>No</i>
Provides full range of signal types for realistic environment modeling: <ul style="list-style-type: none"> Digital • Analog • Magnetic • Mechanical • Hydraulic • Pneumatic Electromagnetic • Inertial • Frictional • Gravitational • Optical • etc. 	<i>Yes</i>	<i>No</i>
Variable fidelity of all VSIL parts – pay for only the fidelity you need.	<i>Yes</i>	<i>No</i>
All parts within entire system environment can incorporate failure modes that may be selectively manifested under test control.	<i>Yes</i>	<i>No</i>
Intuitive verification testing through software scripting environment.	<i>Yes</i>	<i>No</i>
“One-click” full regression test to re-verify all code following changes.	<i>Yes</i>	<i>No</i>
Used on multiple NASA software assurance research projects.	<i>Yes</i>	<i>No</i>

A Triakis VSIL
takes you all the way to certified, zero-defect software
minus the expense of development and test support hardware